Title: Mechanism for Recognizing and Abstracting Memory Structures

Inventor(s): Alok Jain, et al. Serial No.: NYA Docket No. 15448-0505

15448-0505

1/10 Clk -156 162 - 160 Sel 1 150(1) - 164 116(1) 110(1) 118(1) 120(1) 112(1) 114(1) 122(1) Sel n 150(n) --116(n) - 110(n) 140 142 Bit Bitbar 118(n) √ 120(n) 114(n) 112(n) 122(n) Ц - 182 - 180 **- 170** 172 WE Fig. 1 Datain 190 Dataout

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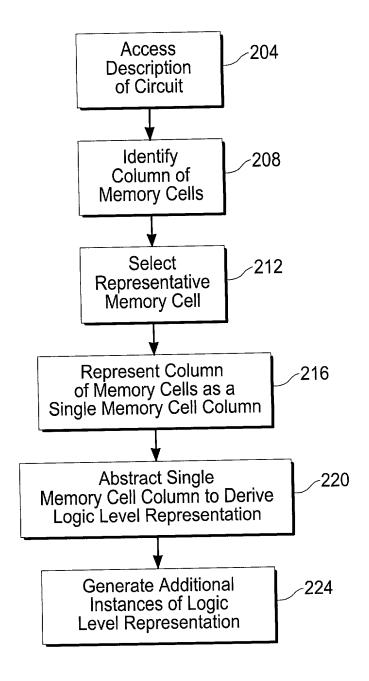
Inventor(s): Alok Jain, et al.

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15448-0505

Docket No. 15448-0505

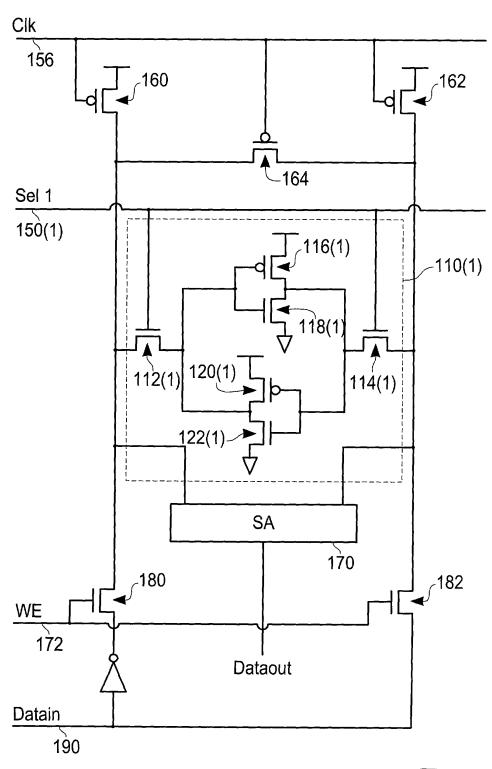
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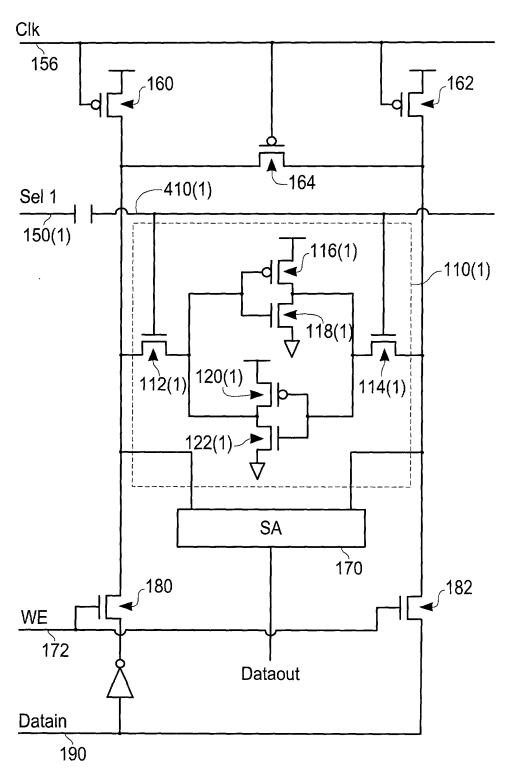
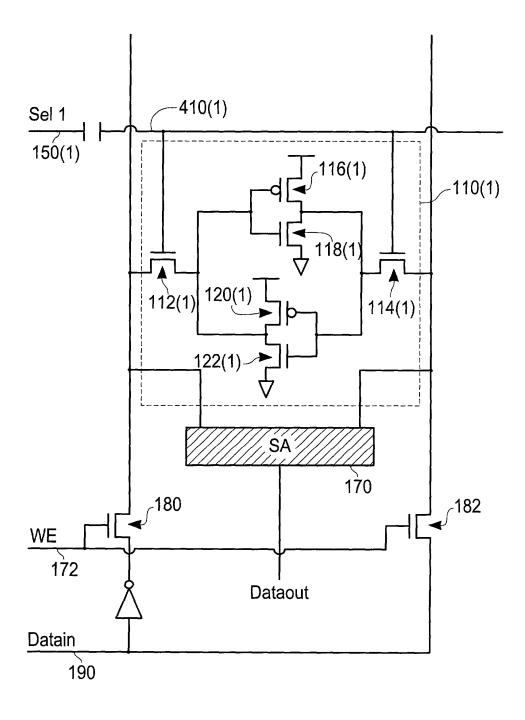


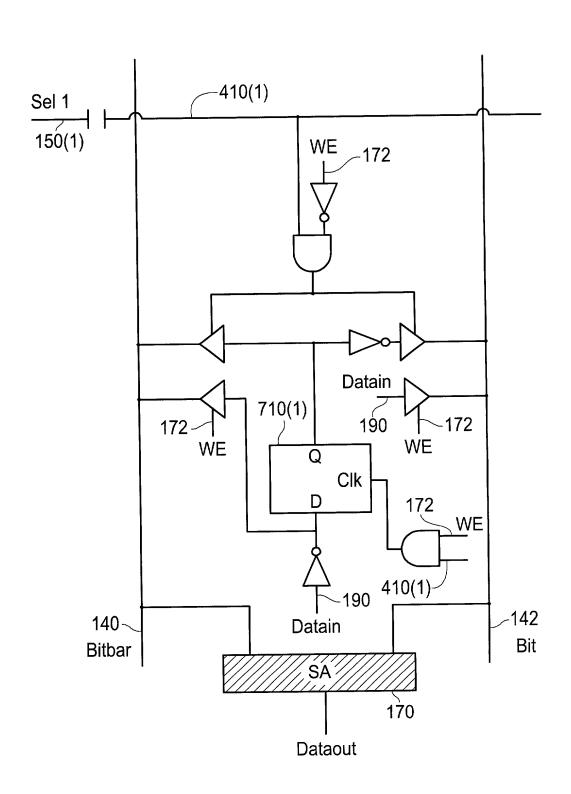
Fig. 4

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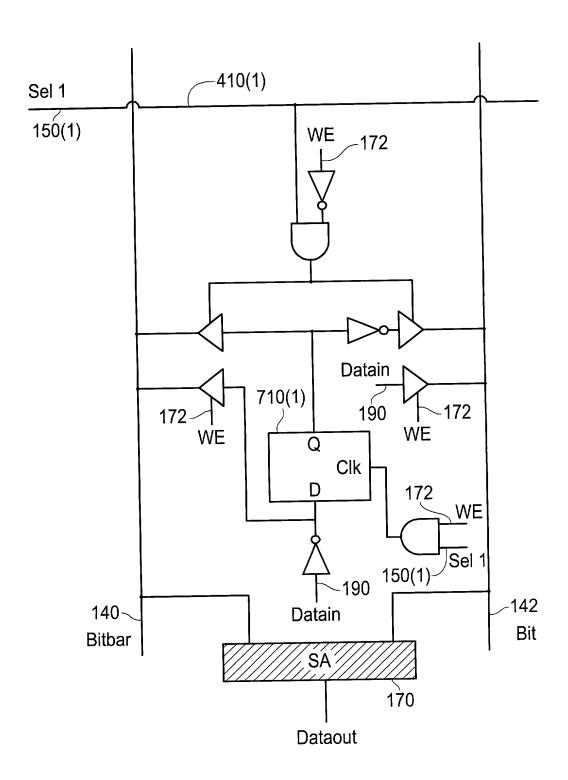
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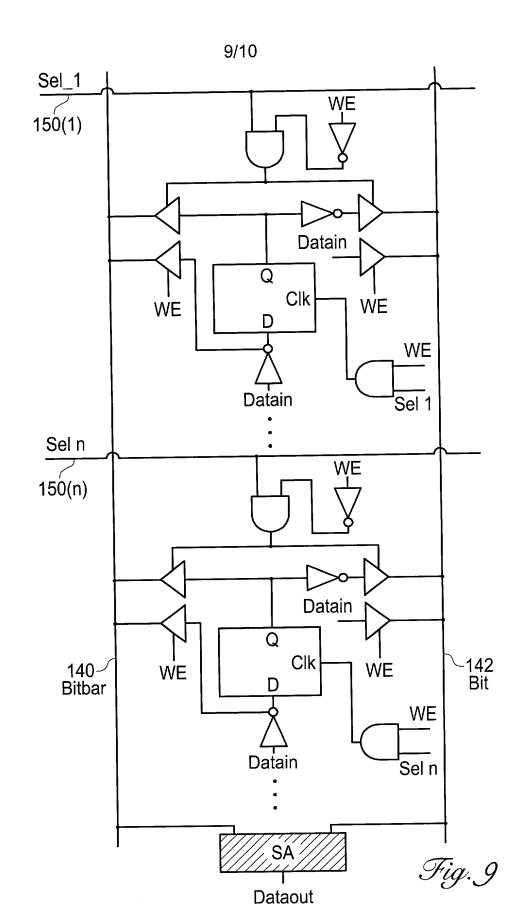




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10/10 / LOCAL NETWORK / 1022 INTERNET HOST 1024 SP SERVER 1030 1026 NETWORK EIK 1000 STORAGE DEVICE 1010 1002 COMMUNICATION INTERFACE 1018 1008 ROM BUS Fig. 10 PROCESSOR 1004 MAIN MEMORY 1006 INPUT DEVICE DISPLAY 1012 CURSOR CONTROL 1016 1014